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APPLICANTS Yoshihiro Kawakita, Osaka, JAPAN; Toshiaki Takenaka, Kyoto, JAPAN; Tadashi Tojyo, Osaka, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP04/17743 11/30/2004				
** FOREIGN APPLICATIONS ***** JAPAN 2003-411456 12/10/2003				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY JAPAN	SHEET(S) DRAWING 7	TOTAL CLAIMS 15
Verified and Acknowledged <u>CA</u> Examiner's Signature Initials				INDEPENDENT CLAIMS 1
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TITLE Method of manufacturing circuit board				
FILING FEE RECEIVED 1450	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	